



IN THE U.S. PATENT AND TRADEMARK OFFICE

In re Application of

Robert B. Staszewski, et al.

Serial No.: 10/008,462

Filed: 11/30/2001

For: FREQUENCY SYNTHESIZER WITH PHASE RESTART

Docket No.: TI-33175

Art Unit: 2637

Examiner: Corrielus, Jean B

Conf. No.: 5009

AMENDMENT TRANSMITTAL FORM

Commissioner for Patents

Alexandria, VA 22313-1450

Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)

I hereby certify, that on this date, this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450 on

August 31, 2005

Elizabeth Austin

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated as shown below:

CLAIMS AS AMENDED						
	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE	ADDITIONAL FEE
Total Claims	18	Minus	20	= 0	x \$ 50 =	\$ 0.00
Independent Claims	2	Minus	3	= 0	X \$200 =	\$ 0.00
TOTAL ADDITIONAL FEE FOR THIS AMOUNT						\$ 0.00

Charge the total additional fee, and any further fees, or credit overpayment to deposit account no. 20-0668. An original and two copies of this sheet are enclosed.

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LETTER PROPOSING DRAWING AMENDMENT UNDER RULE 123

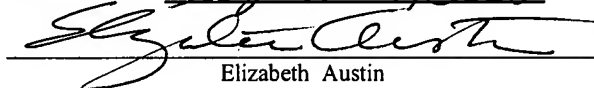
Commissioner for Patents

Alexandria, VA 22313-1450

Dear Sir:

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Elizabeth Austin

Responsive to the Office Action of May 31, 2005, Applicants propose amending Figure 12a to clarify the invention. Applicants propose:

1) adding a signal line from the output of DCO 12, which outputs differential clock signal CKV, to an input of Phase Detection Circuit 60. Support for this amendment is found in paragraph [0033] and Figure 1.

2) adding a signal line, coupled to receive a signal CTL_SRST, to an input of Phase Detection Circuit 60. Support for this amendment is found in paragraph [0056] and Figures 13 & 14 which show synchronous reset CTL_SRST.

3) adding a signal line, enabled to carry a mode switch signal OA_ZPR, from an output of control circuit 26 to an input of Phase Detection Circuit 60. Support for this amendment is found in paragraphs [0066-0067] and Figure 14.

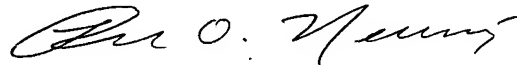
4) adding a signal line, enabled to carry a signal OP_ZPR, from an output of control circuit 24 to an input of Phase Detection Circuit 60. Support for this amendment is found in paragraphs [0065 & 0067] and Figure 14.

5) adding a signal line, coupled to receive a signal CTL_MODE_P, to an input of control circuit 24. Support for this amendment is found in paragraphs [0064-0065] and Figure 13.

6) adding a signal line, coupled to receive a signal CTL_MODE_A, to an input of control circuit 26. Support for this amendment is derived from Figure 13 which shows CTL_MODE_P for the PVT oscillator controller or interface 24. This equivalency is established in [0066]. The acquisition controller 26, as described in [0068], is significantly identical to the controller 24, and has an equivalent mode switch signal. Accordingly, Figure 12a has been amended to show the CTL_MODE_P and CTL_MODE_A signals entering OP 24 and OP 26 circuits, respectively.

All proposed amendments to Claim 12a are illustrated in red ink. No new matter has been added. Applicants respectfully request approval.

Respectfully submitted,



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LETTER TO THE OFFICIAL DRAFTS PERSON

Commissioner for Patents
Attn: Official Drafts Person
Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)

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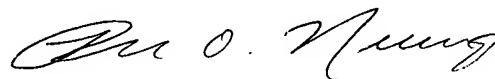
Dear Sir:

Submitted herewith are 10 sheets of formal drawings.

The enclosed drawings are being provided in a timely manner therefore no additional fee is required.

No fee is believed by Applicant to be due; however, the Commissioner is hereby authorized and requested to charge any necessary fee to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



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